

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Shinya Hasegawa

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SEMICONDUCTOR INTEGRATED CIRCUIT AND FUNCTIONAL

BLOCK OF THE SAME

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I hereby certify that this paper is being deposited with the United States Postal Service "EXPRESS MAIL Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Box PATENT APPLICATION, Commissioner for Patents; Washington, DC 20231.

Rosa A. Caviedes

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents Washington, DC 20231

Dear Sir:

Pursuant to 37 CFR §1.56(a), Applicant hereby cites the following documents (copies enclosed) listed on the attached copy of Form PTO-1449.



The Commissioner is also authorized to charge any deficiencies in fees and credit any overpayment of fees to Deposit Account No. 07-1896.

Respectfully submitted,
GRAY CARY WARE & FREIDENRICH LLP

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